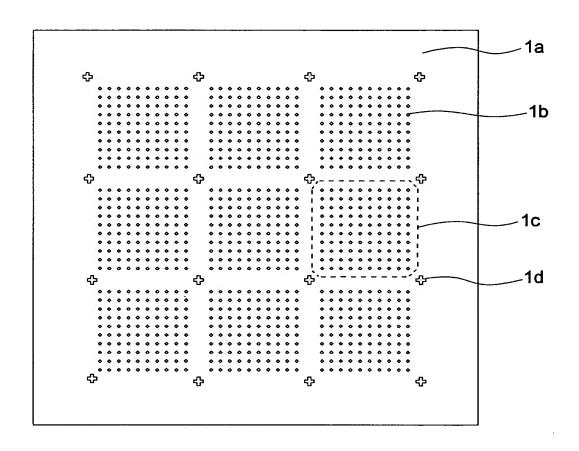
Inventor: Atsushi MURAGUCHI et al.

Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING

METHOD NEW SHEET

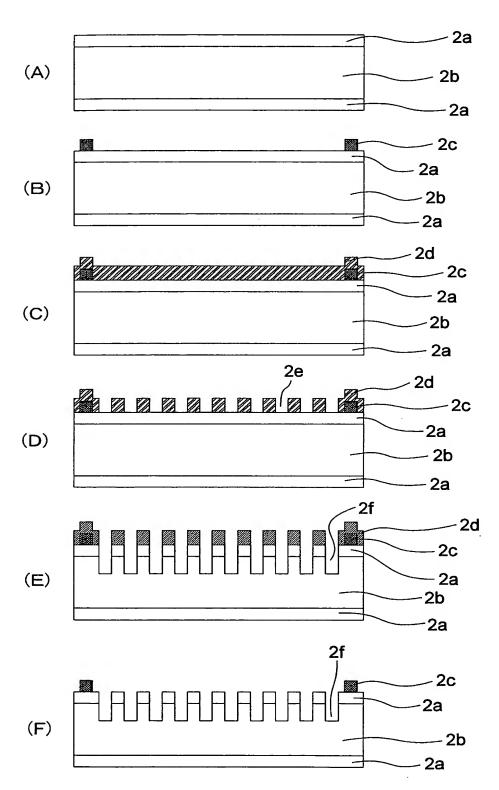
Sheet 1 of 16

図1



App No.: NEW Docket No.: 2870-0330PUS1
Inventor: Atsushi MURAGUCHI et al.
Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING
METHOD
NEW SHEET Sheet 2 of 16

図2 (1)



App No.: NEW Docket No.: 2870-0330PUS1

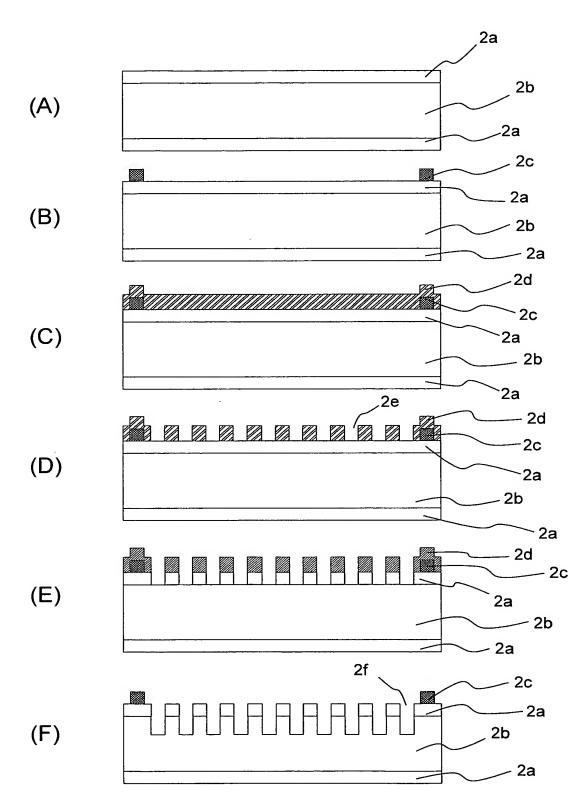
Inventor: Atsushi MURAGUCHI et al.

Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING

METHOD NEW SHEET

Sheet 3 of 16,

図2 (Ⅱ)



App No.: NEW Docket No.: 2870-0330PUS1
Inventor: Atsushi MURAGUCHI et al.
Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING
METHOD
NEW SHEET Sheet 4 of 16

図3

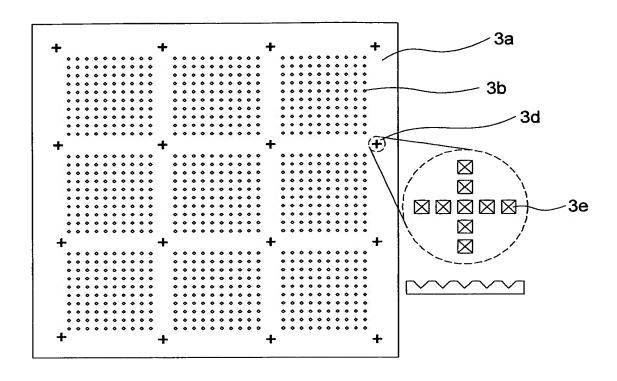
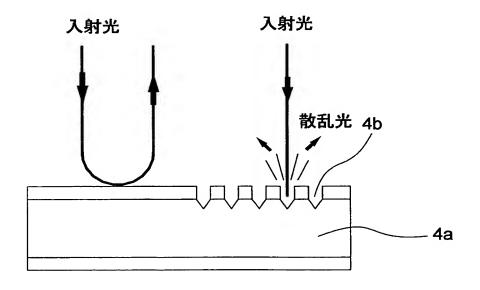


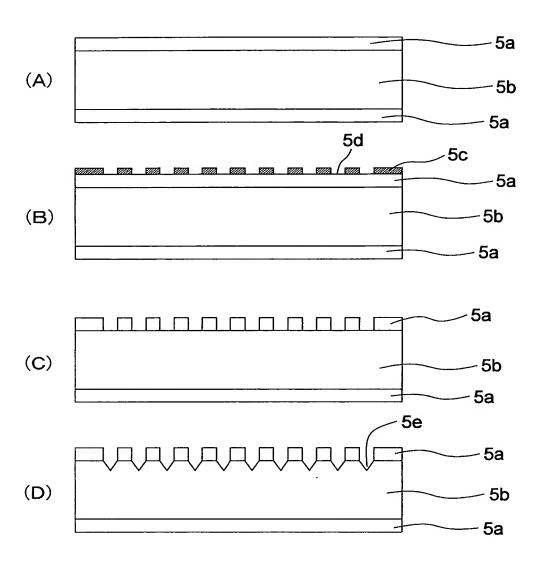
図4



App No.: NEW Docket No.: 2870-0330PUS: Inventor: Atsushi MURAGUCHI et al.
Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING METHOD
NEW SHEET Sheet 5 of 16 Docket No.: 2870-0330PUS1

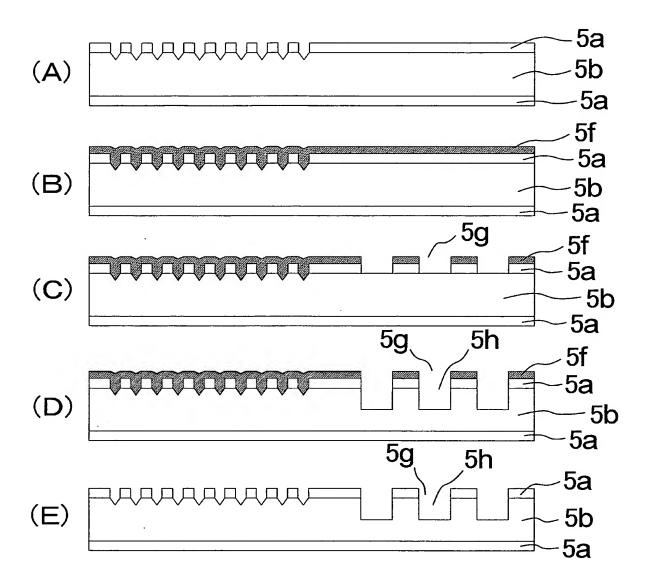
Sheet 5 of 16

図 5



App No.: NEW Docket No.: 2870-0330PUS1
Inventor: Atsushi MURAGUCHI et al.
Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING
METHOD
NEW SHEET Sheet 6 of 16

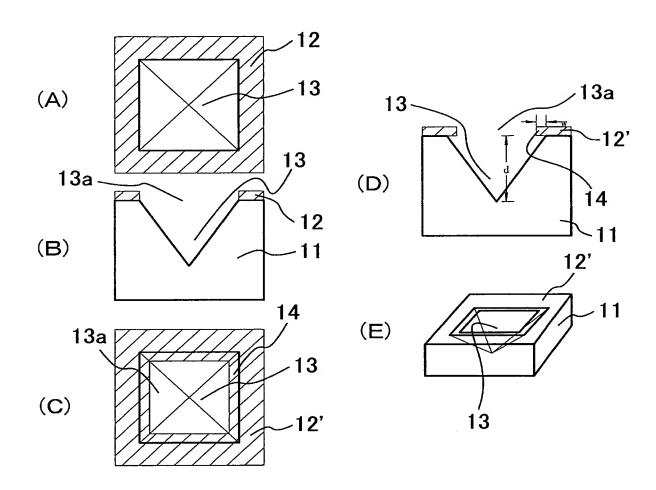
図6



App No.: NEW Docket No.: 2870-0330PUS Inventor: Atsushi MURAGUCHI et al.
Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING METHOD
NEW SHEET Sheet 7 of 16 Docket No.: 2870-0330PUS1

Sheet 7 of 16

図 7

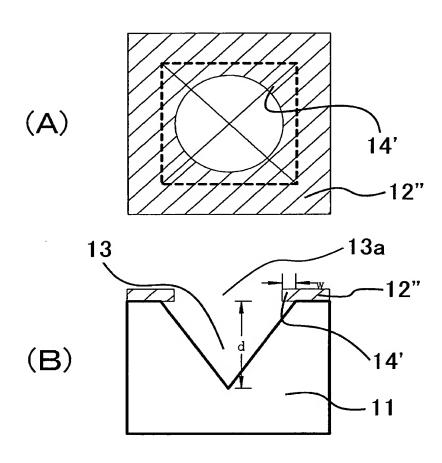


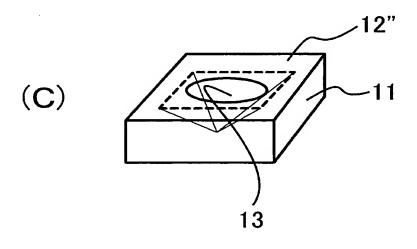
Docket No.: 2870-0330PUS1 App No.: NEW Docket No.: 2870-0330PUS Inventor: Atsushi MURAGUCHI et al. Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING

METHOD NEW SHEET

Sheet 8 of 16

図8



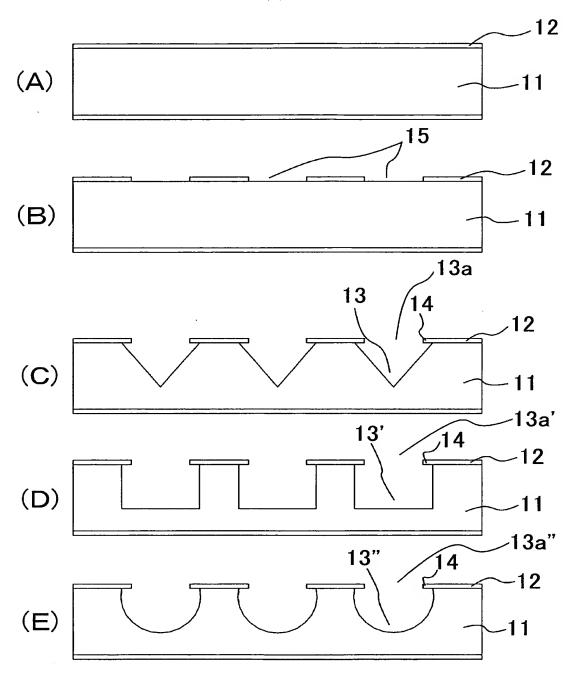


Inventor: Atsushi MURAGUCHI et al.
Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING

METHOD NEW SHEET

Sheet 9 of 16

図 9



Docket No.: 2870-0330PUS1 App No.: NEW Docket No.: 2870-0330PUS Inventor: Atsushi MURAGUCHI et al. Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING

METHOD NEW SHEET Sheet 10 of 16

図10

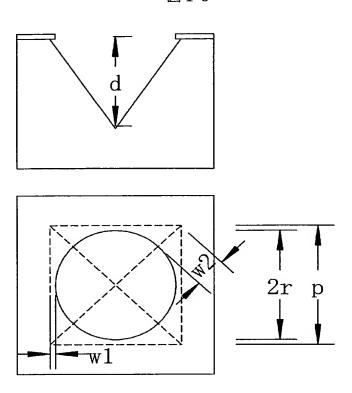
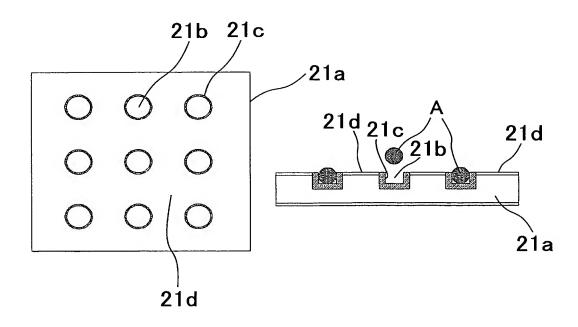


図11



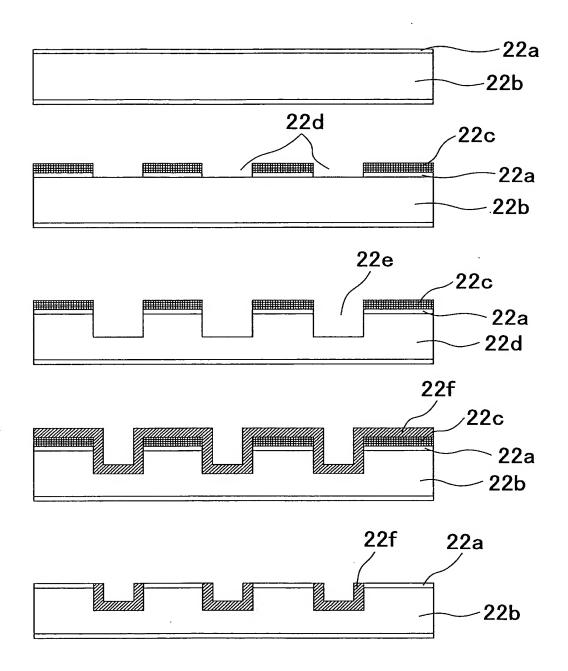
Inventor: Atsushi MURAGUCHI et al.

Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING

METHOD NEW SHEET

Sheet 11 of 16

図12

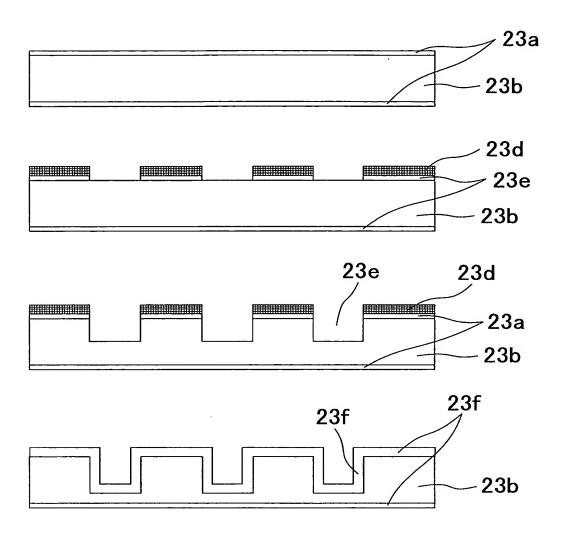


Docket No.: 2870-0330PUS1 App No.: NEW Docket No.: 2870-0330PUS Inventor: Atsushi MURAGUCHI et al. Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING METHOD

NEW SHEET

Sheet 12 of 16

図13



App No.: NEW Docket No.: 2870-0330PUS1

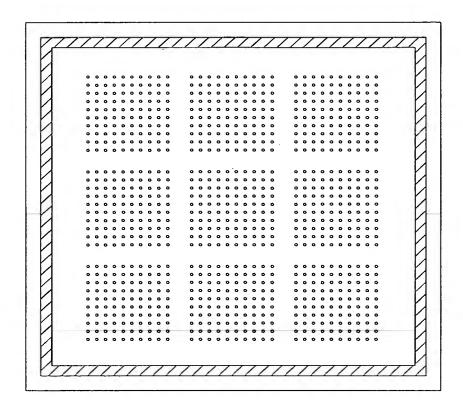
Inventor: Atsushi MURAGUCHI et al.

Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING

METHOD NEW SHEET

Sheet 13 of 16;

図14



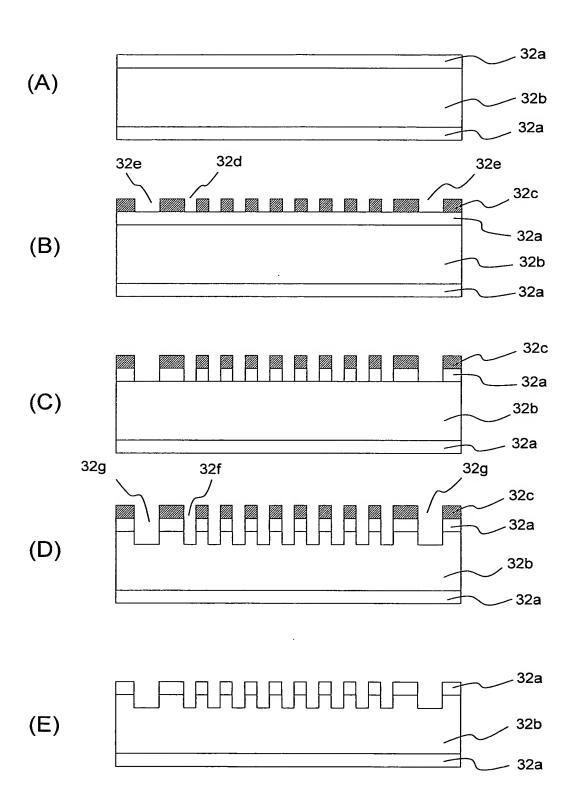
Docket No.: 2870-0330PUS1 App No.: NEW DINVENTOR: Atsushi MURAGUCHI et al.

Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING

METHOD NEW SHEET

Sheet 14 of 16

図15



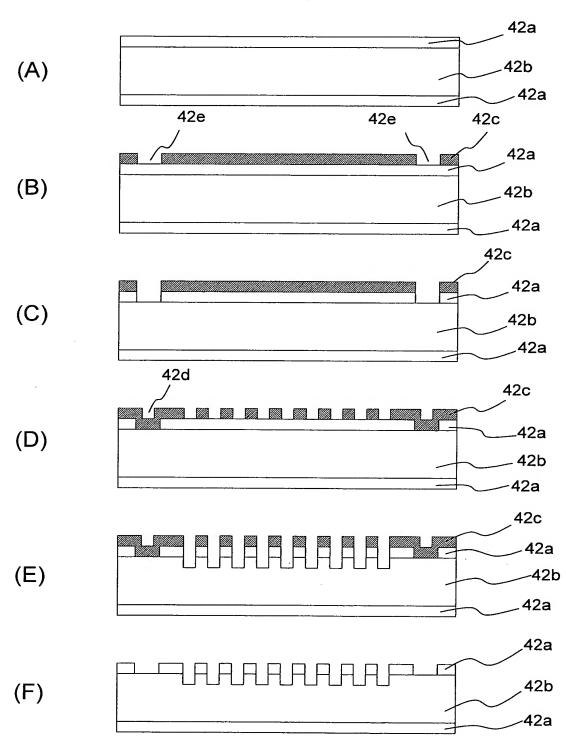
Inventor: Atsushi MURAGUCHI et al.

Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING

METHOD NEW SHEET

Sheet 15 of 16,

図16



App No.: NEW Docket No.: 2870-0330PUS1
Inventor: Atsushi MURAGUCHI et al.
Title: MICROWELL ARRAY CHIP AND ITS MANUFACTURING Docket No.: 2870-0330PUS1

METHOD **NEW SHEET**

Sheet 16 of 16



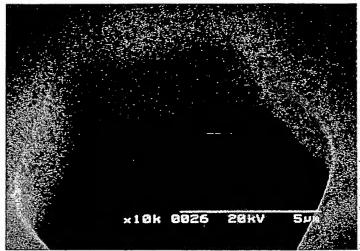
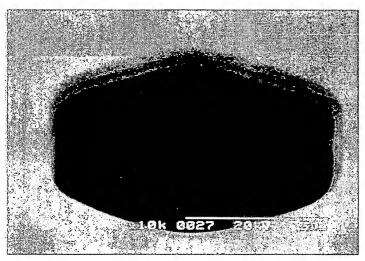


図18



This Page is Inserted by IFW Indexing and Scanning Operations and is not part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:	
☐ BLACK BORDERS	
☐ IMAGE CUT OFF AT TOP, BOTTOM OR SIDES	
☐ FADED TEXT OR DRAWING	
☐ BLURRED OR ILLEGIBLE TEXT OR DRAWING	
SKEWED/SLANTED IMAGES	
COLOR OR BLACK AND WHITE PHOTOGRAPHS	
☐ GRAY SCALE DOCUMENTS	
☐ LINES OR MARKS ON ORIGINAL DOCUMENT	
☐ REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY	

IMAGES ARE BEST AVAILABLE COPY.

As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.